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PATENTS
LT-5 REISSUE

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION FOR REISSUE
OF U.S. PATENT 4,823,070

Date of Issue	:	April 18, 1989
Inventor	:	Carl T. Nelson
Title	:	SWITCHING VOLTAGE REGULATOR CIRCUIT
Assignee	:	Linear Technology Corporation
Reissue Serial No.	:	07/683,549
Reissue Filing Date	:	April 10, 1991
Examiner	:	Kristine L. Peckman
Group Art Unit	:	2102

March 9, 1995

Hon. Commissioner of Patents
and Trademarks
Washington, D.C. 20231

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, applicants, through their attorney, hereby make the patents and other documents listed hereinbelow of record in the above-identified patent application for reissue.

These patents and other documents were previously made of record in a related application, Serial No. 932,014, which issued as patent No. 4,755,741 on July 5, 1988 and/or present in one or more of three subsequent reexaminations of that patent (two of which reexaminations are still pending in a merged proceeding). The present application for reissue is related to these other applications, in that patent No. 4,823,070, which is the subject of the reissue application, incorporates the disclosure of patent No. 4,755,741 by reference (see column 5, line 8-13).

For the convenience of the Examiner, the references have been listed on the following pages, and grouped according to the case in which they were originally cited.

Documents of record in Application Serial No. 932,014:

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4,564,769	Melamed	1/1986
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Other Documents

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Data Sheet, "LM117/LM217/LM317 3-Terminal Adjustable Regulator," National Semiconductor Corporation Linear Databook, 1982, pp. 1-23 to 1-30.

Application Note AN-110, "Fast IC Power Transistor with Thermal Protection," National Semiconductor Corporation, May 1974, pp. AN110-1 to AN110-6.

Data Sheet, "LT1005M/LT1005C Logic Controlled Regulator," Linear Technology Corporation, Linear Databook, 1985, pp. 259-270.

Data Sheet, "LT1001 Precision Operational Amplifier," Linear Technology Corporation, Linear Databook, 1985, pp. 24-35.

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Documents of record in Reexam Serial No. 90/002,035:

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Data Sheet, "LM119/LM219/LM319 High Speed Dual Comparator," National Semiconductor Corporation Linear Databook, 1982, pp. 5-22 to 5-26.

Data Sheet, "LM111/LM211 Voltage Comparator," National Semiconductor Corporation Linear Databook, 1982, pp. 5-16 to 5-21.

Dobkin, "Monolithic Temperature Transducer," 1974 IEEE International Solid-State Circuits Conference, Digest of Technical Papers, pp. 126-127, 239.

Documents of record in Reexam Serial No. 90/003,419:

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a	4,564,769	Melamed	1/1986
	3,617,845	McKenna	11/1971
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	4,213,068	Ahmed	7/1980
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	4,220,873	Giordano	9/1980
	4,234,805	Carlsen II	11/1980
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a Knickmeyer, K. H., "Dynamic Transistor Antisaturation Control," IBM Technical Disclosure Bulletin, Vol. 17, No. 4, September 1974, pp. 1091-1092.

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Grebene, Alan. B., "Bipolar and MOS Analog Integrated Circuit Design," John Wiley & Sons, 1984, pp. 183-187.

b Ferranti, "Collector Diffusion Isolation - A New Bipolar Process for Integrated Circuits," 2nd Ed., Ferranti Ltd., 1982.

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a Linear Technology LT1001 Operational Amplifier, 1985.

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Documents of record in Reexam Serial No. 90/003,561:

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c	4,337,494	Huykman	6/1982
c	4,286,175	Baker	8/1981
c	3,617,845	McKenna	11/1971
c	4,215,279	Lataire et al.	7/1980
c	4,309,645	De Villenvue	1/1982
c	4,441,068	Smith	4/1984
c	4,156,837	Baker	5/1979
c	4,442,411	Gehring	4/1984
c	4,533,839	Balakrishnan	8/1985
c	4,480,201	Jaeschke	10/1984

Other Documents

c IBM Technical Disclosure Bulletin, Vol. 21, No. 8, january 1979.

c Redl, "Optimizing Dynamic Behavior with Input and Output Feed-Forward and Current-Mode Control," Proceedings of Powercon 7, March 1980.

Note:
Several of the above patents and documents were cited more than once. Duplicate citations are marked with a note indicating the case in which the reference was first cited. Only one copy of each patent and document is enclosed with this information disclosure statement.

a first cited in Application Serial No. 932,014
b first cited in Reexam Serial No. 90/002,035
c first cited in Reexam Serial No. 90/003,419

It is respectfully requested that the above patents and other documents be (1) fully considered by the Patent and Trademark Office during the examination of this application; and (2) printed on any patent which may issue on this application. Applicants enclose two copies of Form PTO-1449, and request that one copy of, as considered and initialed by the Examiner, be returned with the next communication.

In accordance with 37 C.F.R. § 1.97(h), applicants do not by this disclosure admit, or imply, that the foregoing references by themselves, or in combination with any other information, establish a prima facie case of unpatentability of a claim. In addition, applicants expressly reserve the right to establish invention prior to the effective date of the references cited, pursuant to 37 C.F.R. § 1.131.

An early and favorable action is respectfully requested.

Respectfully submitted,

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I Hereby Certify that this Correspondence is being Deposited with the U. S. Postal Service as First Class Mail in an Envelope Addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on March 9, 1995

Dalene Gulachon-Rosen
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